FILED VIA FACSIMILE

PATENT APPLICATION
Docket No: 15436.928.11

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of		)
	Sudeep Bhoja et al.	)
Serial No.:	09/954,453	) Art Unit 2115
Filed:	September 12, 2001	)
Patent No.:	6,934,869	)
Issued:	August 23, 2005	)
For:	METHOD AND APPARATUS FOR ELIMINATING DEAD ZONE IN PHASE LOCKED LOOPS USING BINARY…	)
Customer No.:	022913	)

REVOCATION AND SUBSTITUTE POWER OF ATTORNEY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

I, the undersigned, Stephen K. Workman, state that I am the Senior Vice President of Finance and the CFO of Finisar Corporation and that I am authorized to execute this Revocation and Substitute Power of Attorney on behalf of Finisar Corporation.

I further state that Finisar Corporation is the assignee of the entire interest of the above-identified patent as shown by the assignment recorded in the U.S. Patent and Trademark Office at the Reel and Frame identified in Exhibit A and assignments identified in Exhibit B. The assignee, Finisar Corporation, hereby revokes all previous powers of attorney in the above-identified patent, and now hereby appoints all attorneys under:

CUSTOMER NUMBER: 022913

of WORKMAN NYDEGGER as attorney with full power of substitution and revocation, to prosecute said application, to make alterations and amendments therein, to receive the Letters Patent, and to transact all business in the Patent and Trademark Office connected therewith.

All correspondence and telephonic communication should be directed to:

ERIC L. MASCHOFF

at the address associated with the above-identified customer number.

This Revocation and Substitute Power of Attorney and Statement under 37 C.F.R. 3.73(b)(1) is effective for the above-identified patent, and shall be filed at the U.S. Patent & Trademark Office.

Signed this 16 day of MARCH, 2006.

By: S. K. Workman

Stephen K. Workman
Sr. Vice President Finance and CFO
Finisar Corporation
1389 Moffett Park Drive
Sunnyvale, CA 94089

Finisar Legal

EXHIBIT A

EXHIBIT A

A chain of title of U.S. Patent No. 6,934,869, issued August 23, 2005, is shown in an assignment from the inventor(s) to Big Bear Networks, Inc. recorded at Reel 012180, Frame 0933 and an assignment from Big Bear Networks, Inc. to Finisar Corporation recorded at Reel 017057, Frame 0222.

EXHIBIT B

EXHIBIT B Schedule of U.S. Patent Applications Assigned From Big Bear Networks, Inc. to Finisar Corporation			
WN Docket No.	**Date**	**Title And Named Inventors**	**Document Number**
15436.928.4	Filed 08/30/2002	METHOD AND APPARATUS FOR DATA MULTIPLEXING Derek Shaeffer et al.	60/407558
15436.928.4.1	Filed 07/17/2003	METHOD AND APPARATUS FOR DATA MULTIPLEXING Derek Shaeffer et al.	10/623,028
15436.928.6	Issued 05/20/2003	TRI-ELECTRODE TRAVELING WAVE OPTICAL MODULATORS & METHODS Marc E. Hill et al.	6,567,203
15436.928.7	Issued 10/29/2002	METHOD AND APPARATUS FOR ASYMMETRICAL OPTICAL MODULATION Jonathan Paul King	6,473,219
15436.928.8	Issued 05/03/2005	AUTOMATIC CONFIGURATION AND OPTIMIZATION OF OPTICAL TRANSMISSION USING RAW ERROR RATE MONITORING Laura Ellen Adams et al.	6,889,347
15436.928.9	Issued 04/22/2003	METHOD AND SYSTEM FOR COMPENSATION OF LOW-FREQUENCY PHOTODIODE CURRENT IN A TRANSIMPEDIANCE AMP Shanthi Pavan et al.	6,552,615
15436.928.10	Issued 05/18/2004	DUAL-ELECTRODE TRAVELING WAVE GUIDE OPTICAL MODULATORS AND METHODS Marc E. Hill et al.	6,738,174
15436.928.11	Issued 08/23/2005	METHOD AND APPARATUS FOR ELIMINATING DEAD ZONE IN PHASE LOCKED LOOPS USING BINARY QUANTIZED PHASE DETECTORS Sudeep Bhoja et al.	6,934,869
15436.928.12	Issued 04/15/2003	APPARATUS AND METHOD FOR HERMETICALLY SEALING AND EMI SHIELDING INTEGRATED CIRCUITS FOR HIGH SPEED ELECTRONIC PACKAGES Yu Ju Chen et al.	6,548,893

EXHIBIT B Schedule of U.S. Patent Applications Assigned From Big Bear Networks, Inc. to Finisar Corporation			
WN Docket No.	**Date**	**Title And Named Inventors**	**Document Number**
15436.928.13	Issued 02/24/2004	HIGH SPEED PACKAGED WITH SUSPENDED SUBSTRATE AND PCB Yu Ju Chen et al.	6,697,260
15436.928.15	Issued 03/02/2004	METHOD AND APPARATUS FOR PRODUCING HIGH FREQUENCY OSCILLATIONS Derek Shaeffer	6,700,452
15436.928.15.1	Issued 09/20/2005	METHOD AND APPARATUS FOR PRODUCING HIGH FREQUENCY OSCILLATIONS Derek Shaeffer	6,946,921
15436.928.20	Issued 08/08/2006	METHOD AND SYSTEM FOR CHARACTERIZING OPTICAL RECEIVERS BY MATHEMATICAL DERIVATION Sumit Verma et al.	7,089,161
15436.928.21	Issued 08/22/2006	DUAL-ELECTRODE TRAVELING WAVE OPTICAL PHASE SHIFTERS AND METHODS Marc E. Hill et al.	7,095,543